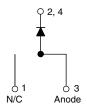


HEXFRED® Ultrafast Soft Recovery Diode, 4 A





PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	4 A						
V_{R}	600 V						
V _F at I _F	1.8 V						
t _{rr} typ.	17 ns						
T _J max.	150 °C						
Diode variation	Single die						

FEATURES

- Ultrafast recovery time
- Ultrasoft recovery
- Very low I_{RRM}
- Very low Q_{rr}
- · Guaranteed avalanche
- Specified at operating temperature
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

BENEFITS

- Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for freewheeling, flyback, power converters, motor drives, and other applications where high speed and reduced switching losses are design requirements.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Cathode to anode voltage	V_{RRM}		600	V				
Maximum continuous forward current	I _{F(AV)}	T _C = 100 °C	4					
Single pulse forward current	I _{FSM}		25	Α				
Repetitive peak forward current	I _{FRM}	T _C = 116 °C	16					
Maximum power dissipation	P_D	T _C = 100 °C	10	W				
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 150	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
		I _F = 4 A	-	1.5	1.8	V	
Forward voltage See fig. 1	V _F	I _F = 8 A	-	1.8	2.2		
occ ng. 1		I _F = 4 A, T _J = 125 °C	-	1.4	1.7		
Maximum reverse		$V_R = V_R$ rated	-	0.17	3.0		
leakage current	I _R	$T_J = 125 ^{\circ}\text{C}, V_R = 0.8 \text{x} \text{V}_R \text{rated}$	-	44	300	μΑ	
Junction capacitance	C _T	V _R = 200 V	-	4	8	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, dI_F/dt =$	200 A/ μ A, V _R = 30 V	-	17	-			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	28	42	ns		
		T _J = 125 °C		-	38	57			
B. d		T _J = 25 °C		-	2.9	5.2	۸		
Peak recovery current	I _{RRM}	$ T_1 = 125 ^{\circ}\text{C}$ $ T_1 = 125 ^{\circ}\text{C}$ $ T_2 = 125 ^{\circ}\text{C}$	-	3.7	6.7	Α			
Dougrap rapovani chargo	0	T _J = 25 °C	dl _F /dt = 200 A/μs V _R = 200 V	-	40	60	nC		
Reverse recovery charge	Q_{rr}	T _J = 125 °C		-	70	105	IIC		
Data of fall of our and our and	.1 .11 /.11	.11 /.11	T _J = 25 °C		-	280	-	Λ/μο	
Rate of fall of recovery current	dI _{(rec)M} /dt	T _J = 125 °C		-	235	-	A/μs		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55	-	150	°C		
Thermal resistance, junction to case	R _{thJC}		-	-	5.0	°C/W		
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	80	C/VV		
Weight			-	2.0	-	g		
vveignt			-	0.07	-	OZ.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style D-PAK		HFA04	SD60S			

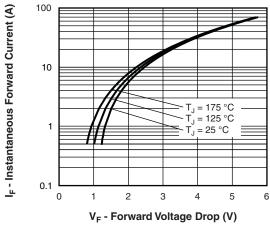


Fig. 1 - Typical Forward Voltage Drop Characteristics

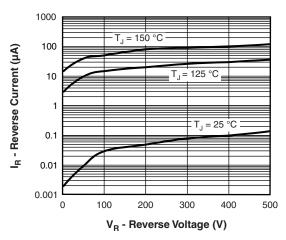


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

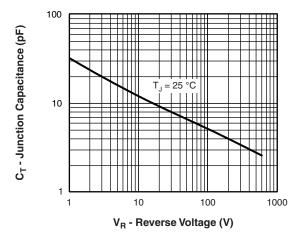


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

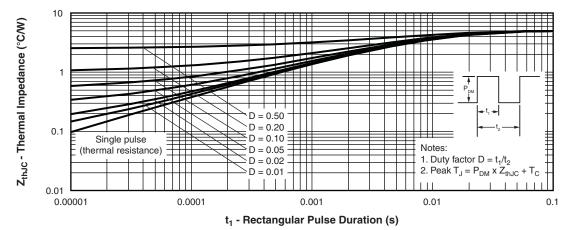


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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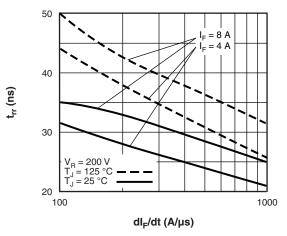


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt

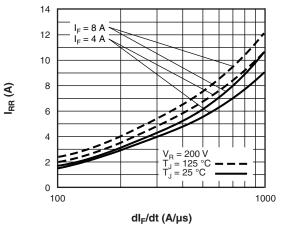


Fig. 6 - Typical Recovery Current vs. dl_F/dt

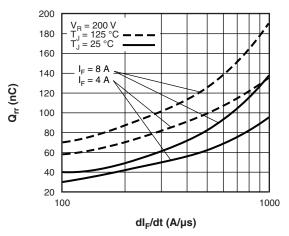


Fig. 7 - Typical Stored Charge vs. dl_F/dt

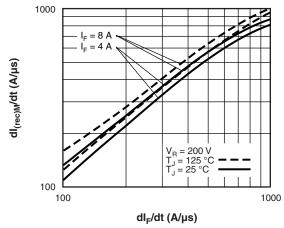


Fig. 8 - Typical dl_{(rec)M}/dt vs. dl_F/dt

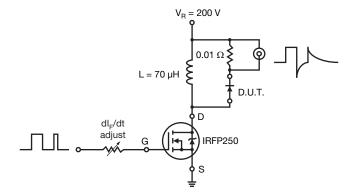
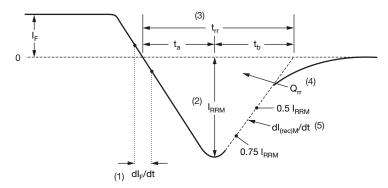


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{r}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

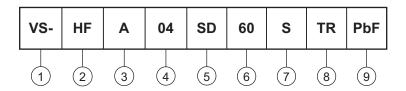
(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - HEXFRED® family

3 - Electron irradiated

- Current rating (04 = 4 A)

5 - D-PAK

Voltage rating (60 = 600 V)

7 - S = D-PAK

8 - • TR = Tape and reel

• TRR = Tape and reel (right oriented)

• TRL = Tape and reel (left oriented)

9 - • PbF = Lead (Pb)-free

• P = Lead (Pb)-free (for TRR and TRL)

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95016						
Part marking information	www.vishay.com/doc?95059						
Packaging information	www.vishay.com/doc?95033						



NOTES

3

2

MAX.

0.410

0.070

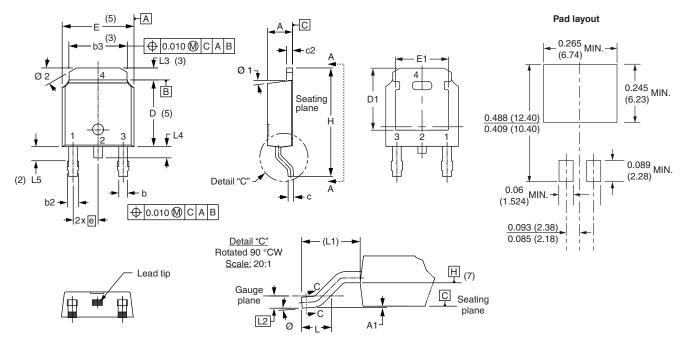
0.050

0.040

0.060

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



Ī	SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES		
		MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX		
Ī	Α	2.18	2.39	0.086	0.094				2.29 BSC		e 2.29 BSC		0.090	BSC
Ī	A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.41		
	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07		
	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.		
Ī	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC		
	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05		
	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04		
Ī	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06		
	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°		
	E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°		
	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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